

## Electronic Acknowledgement Receipt

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| <b>Application Number:</b>                  | 10562869  |
| <b>International Application Number:</b>    |   |
| <b>Confirmation Number:</b>                 | 6418  |
| <b>Title of Invention:</b>                  | Logic gate with a potential-free gate electrode for organic integrated circuits |
| <b>First Named Inventor/Applicant Name:</b> | Walter Fix  |
| <b>Customer Number:</b>                     | 27162   |
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| <b>Filer Authorized By:</b>                 | William Squire  |
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### Payment information:

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| Submitted with Payment | no |
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### File Listing:

| Document Number | Document Description                    | File Name         | File Size(Bytes) /Message Digest                  | Multi Part /.zip | Pages (if appl.) |
|-----------------|---|-------------------|---|------------------|------------------|
| 1               | Information Disclosure Statement Letter | 144suppidsltr.pdf | 29356<br>0e975b88e26def869656e0113f3881c0792bd8fb | no               | 1                |

### Warnings:

### Information:

|   |  |            |   |    |   |
|---|--|------------|---|----|---|
| 2 | Information Disclosure Statement (IDS) Filed | 144ids.pdf | 574086                                  | no | 4 |
|   |  |            | 4ab53a4c46824265d025fd3e8c00904e43de011 |    |   |

**Warnings:**

**Information:**

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|   |                   |                |  |    |    |
|---|-------------------|----------------|--|----|----|
| 3 | Foreign Reference | JP05152560.pdf | 736690                                   | no | 10 |
|   |                   |                | 5c1d7c60746b12ae602b7d5ab4dab2329fd2c3ae |    |    |

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

**National Stage of an International Application under 35 U.S.C. 371**

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